



FACULTY PROFILE

1. Personal Details

NAME	Mr. GANESHA H.S.	
DEPARTMENT	ELECTRONICS & COMMUNICATION ENGINEERING	
DESIGNATION	ASSISTANT PROFESSOR	
PHONE	+91-9964944390	
EMAIL ID	ganeshahs@jssaten.ac.in	
Date of Joining (JSSATEN)	10-08-2017	

2. Experience

Total Experience in Years: 10	Teaching: 10 Years	Industry: Nil	Research: Nil
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3. Qualifications

COURSES	SPECIALIZATION	Year of Award	INSTITUTION	UNIVERSITY
B.E.	Electronics and Communication Engineering.	2009	A.P.S. College of Engineering, Bangalore	Visvesvaraya Technological University (VTU), Belagavi, Karnataka .
M.Tech.	Digital Electronics and Communication	2015	N.M.A.M. Institute of Technology, Nitte, Karnataka	Visvesvaraya Technological University (VTU), Belagavi, Karnataka .
Ph.D.	Artificial Intelligence of Things	Pursuing	Amity School of Engineering and Technology, Noida, Uttar Pradesh.	Amity University, Noida, Uttar Pradesh-201313
Post Doc.	-	-	-	-

4. Research & Publications

Papers Published in Web of Science indexed Journals	International: 00	National: 00
Papers Published in SCOPUS indexed Journals	International: 00	National: 00
Papers Published in other Journals	International: 01	National: 00
Papers Presented in Conferences / Symposium	International: 02	National: 00
Books / Book chapters Published	Name of the book: NIL Publisher: Year of Publication:	

5. Research Guidance

PhD Guide? Give field & University	No	NA
Ph.D.s / Projects Guided	Ph.D. Awarded: No Guiding: NIL	Projects at Master's Level: - Nil Projects at Bachelor's Level: 18

6. Grants

i. Funds Received (Projects)

Project Name	Grant Amount	Date of receiving the Grant	Duration	Grant issuing authority/ Body / Organization
-	-	-	-	-

ii. Patents

Sl. No.	Title Details	Details of award of patent / Filed
-	-	-

iii. Consultancy

Title of the work	Amount in Rs.	Date of receiving the Grant	Duration	Grant issuing authority/ Body / Organization
-	-	-	-	-

7. Awards Received

Awards “Best Paper Award”	Nil
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8. Publications

i. International Journals

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher
1	A 8-Bit Hybrid Architecture Current-Steering DAC	Mr. Ganesha H.S., Dr. Rekha Bhandarkar, Ms. Vijayalatha Devadiga	International Research Journal of Engineering and Technology (IRJET)	Volume: 02 Issue: 04 July-2015	-	-	-

ii. National Journals

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher
-	-	-	-	-	-	-	-

iii. Conferences

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Conference	Volume No. Issue No. Year	WOS / Scopus	Impact Factor	Publisher
1	Multi-Layer Heterogeneous Ensemble Model for Human Activity Recognition	Ganesha H S, Rinki Gupta, Sindhu Hak Gupta	International Conference on Signal Processing and Communication (ICSC)	- (Paper Accepted-2021)	Scopus	-	IEEE
2	Design of Low Power High Speed Area Efficient Wallace Tree Multiplier using Ripple Carry Adder	Ganesha H S Mahendra H N	International Conference on Intelligent Control and Computation for Smart Energy and Mechatronic Systems, 20-21 March 2020 JSSATE NOIDA.	-	-	-	-

iv. Workshops /Conferences Attended

Sl. No.	Name of the workshop / Conference	Organizer	Date
1	Emerging Trends In Signal & Image Processing Applications	Canara engineering college, Mangalore	27th to 29th January 2011
2	CADENCE OrCAD PSPICE/PCB DESIGN-The Analysis and Design of Electronic Circuits And Systems	Canara engineering college, Mangalore	12th to 14th January 2015
3	VLSI Design Using Open Source EDA	SAHYADRI College of Engineering and Management, Mangaluru.	29th to 30th July 2016
4	ARM7 Processor and Keil IDE Workshop	Alva's Institute of Engineering and Technology, Moodbidri.	23rd to 24 January 2017
5	Embedded System Design Using MSP-430	Texas Instruments India University Program at ABESIT, Ghaziabad	4th to 6th December 2017
6	Signal Processing and its Application	J.S.S. Academy of Technical Education, Noida	23rd to 28th April 2018
7	Analog Integrated Circuit Design Using Cadence Analog Design Flow	J.S.S. Academy of Technical Education, Noida	27th August to 01st September 2018
8	Business Analytics-2018	Lloyd Business school, Greater Noida.	15th September 2018
9	Robotics and Embedded Systems	Electronics & ICT Academy IIT Roorkee	02nd to 06th April 2019
10	Pedagogical Transformation in Teacher Education after Pandemic: way Forward	Teacher's Training College Bhagalpur, Bihar- 812003	14th to 15th June, 2020
11	LaTeX & its Applications for Researchers	Dept. of IS&E, Vidyavardhaka College of Engg- 570002	20th to 24th July 2020
12	FDP on Python in association with spoken tutorial IIT Bombay	National Mission on Education through ICT,	22nd June 04th July 2020

		MHRD, Govt. of India	
13	Data Science for All	E&ICT Academics- NIT Warangal, IIITDM Jabalpur & NIT Patna	27th July-08th August 2020
14	Satellite-Image Processing: Feature Extraction	Bangalore Technological Institute, Bengaluru- 560035	27th June 2020
15	NBA Preparation & CO-PO Mapping	SVPM's COE Malegaon Bk, Baramati, in association with ISTE	28th June, 2020
16	Tutorial session in connection with ICCSEMS-2020	JSS Academy of Technical Education, NOIDA	24th September 2020
17	Solar & PV Storage Systems	Department of EEE, JSSATEN NOIDA, Sponserd by AICTE	14th to 19th December 2020
18	Internet of Things and data Analytics	Department of IT, JSSATEN, NOIDA Sponserd by AICTE	02nd to 08th December 2020
19	Deep Learning & its Application (Parallel Architecture)	Electronics and ICT Academies at IIT Guwahati, IIT Kanpur, IIT Roorkee, MNIT Jaipur, NIT Patna, NIT Warangal and PDPM IIITDM Jabalpur	23rd Aug to 3rd September, 2021

v. Workshops / Conference (Organized)

Sl. No.	Name of the workshop / Conference	Organized by	Date	Role
1	Analog Integrated Circuit Design using Cadence Analog Design Flow	Department of Electronics & Communication Engineering, JSSATEN, sponsored by	27 th August– 1 st September, 2018	Faculty Coordinators

		Dr. A. P.J.AbdulKalam Technical University Lucknow under TEQIP-III.		
2	FDP on PYTHON	Department of Electronics & Communication Engineering, JSS Academy of Technical Education, NOIDA, Under Spoken Tutorial Project, IIT Bombay.	22th June – 05 th July, 2020	Faculty Coordinators

vi. Conference Attended (those sponsored by AICTE / ISTE/IETE/TEQIP or any other sponsoring body)

Sl. No.	Name of the workshop / Conference	Organizer	Date
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9. Details of NPTEL / COURSERA courses completed

Sl. No.	Name of the subject	Organized by	Date of completion / Award	Grade / Marks
1	Cloud computing	NPTEL	Jan-Apr 2020	72%
2	Python for everybody	COURSERA	15/07/2021	

10. Membership of Professional Bodies:

Nil

11. Any other information you will like to share about your professional experience

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